# Abstracts

## The first very low-IF RX, 2-point modulation TX CMOS system on chip Bluetooth solution

*C. Durdodt, M. Friedrich, C. Grewing, M. Hammes, A. Hanke, S. Heinen, J. Oehm, D. Pham-Stabner, D. Seippel, D. Theil, S. van Waasen and E. Wagner. "The first very low-IF RX, 2-point modulation TX CMOS system on chip Bluetooth solution." 2001 Radio Frequency Integrated Circuits (RFIC) Symposium 01. (2001 [RFIC]): 99-102.*

The proposed low cost Bluetooth single-chip solution is implemented in a 0.25 /spl mu/m CMOS technology. The "System On Chip (SOC)" includes all necessary baseband- and RF-parts to achieve full Bluetooth functionality, by occupying 18.5 mm/sup 2/ chip area in total. The maximum current consumption of the analog part is 60 mA. The internal regulated supply voltages of the analog and digital parts are 2.65 V. First measurement results of basic functionalities are discussed in this paper.

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